

FIG . 2

09732804-120800

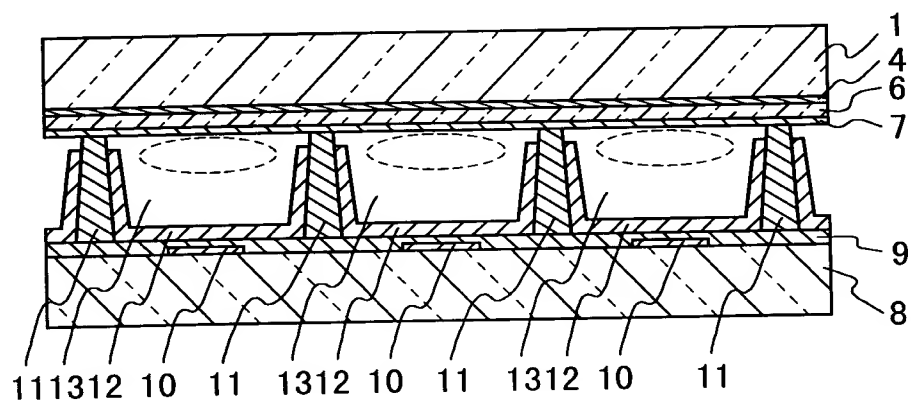


FIG . 3

The diagram illustrates a crossbar switch architecture with N columns and M rows. The columns are labeled $D1, D2, D3, D4, \dots, DN-2, DN-1, DN$. The rows are labeled $SCN1, SCN2, SCN3, \dots, SCNM-1, SCNM$. The connections between columns and rows are shown as a grid of dashed lines. Specific connections are labeled $SUS1, SUS2, SUS3, \dots, SUSM-1, SUSM$.

FIG . 4

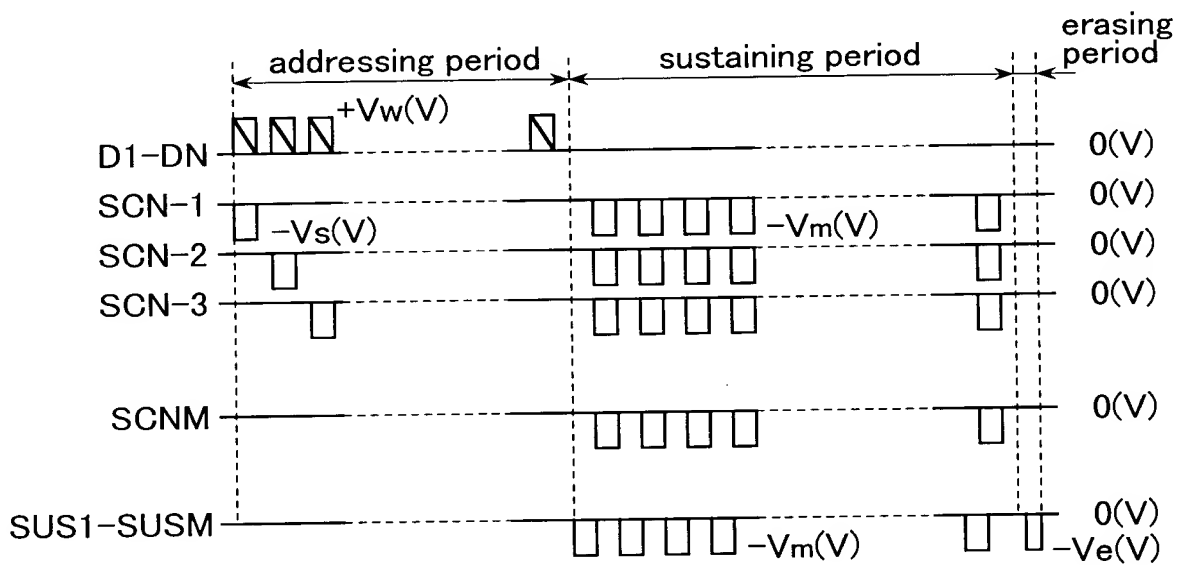


FIG . 5

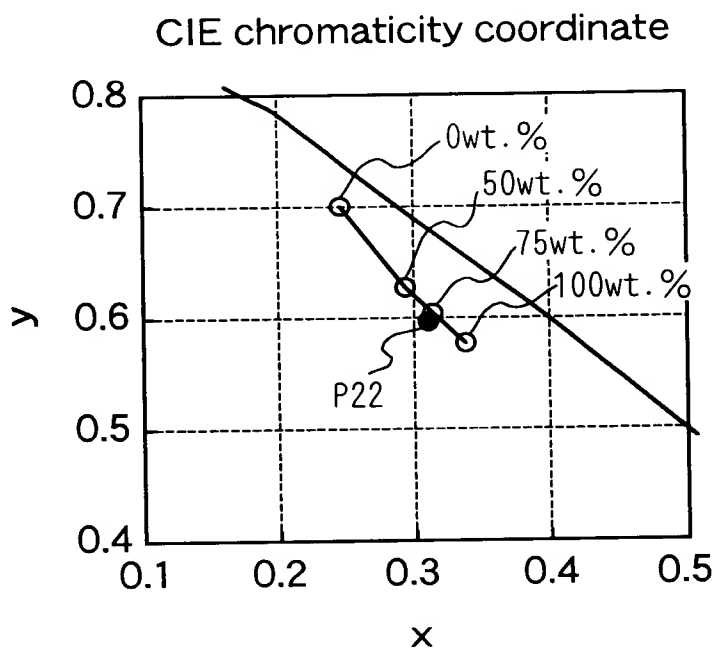


FIG. 6

FIG . 7A

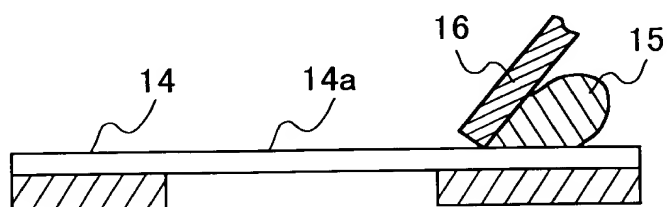


FIG . 7B

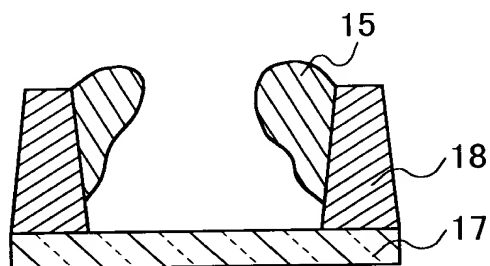


FIG . 7C

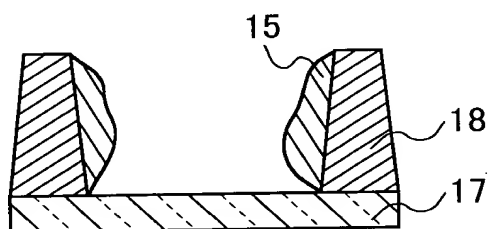


FIG . 7D

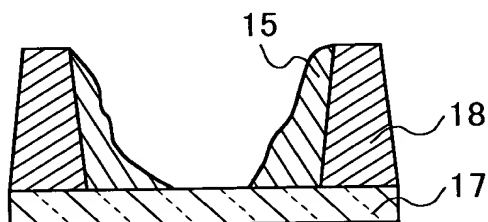
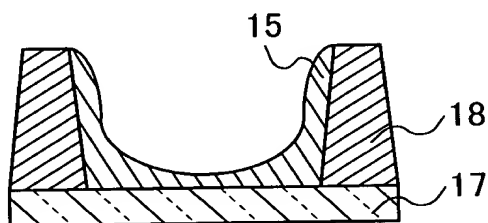


FIG . 7E



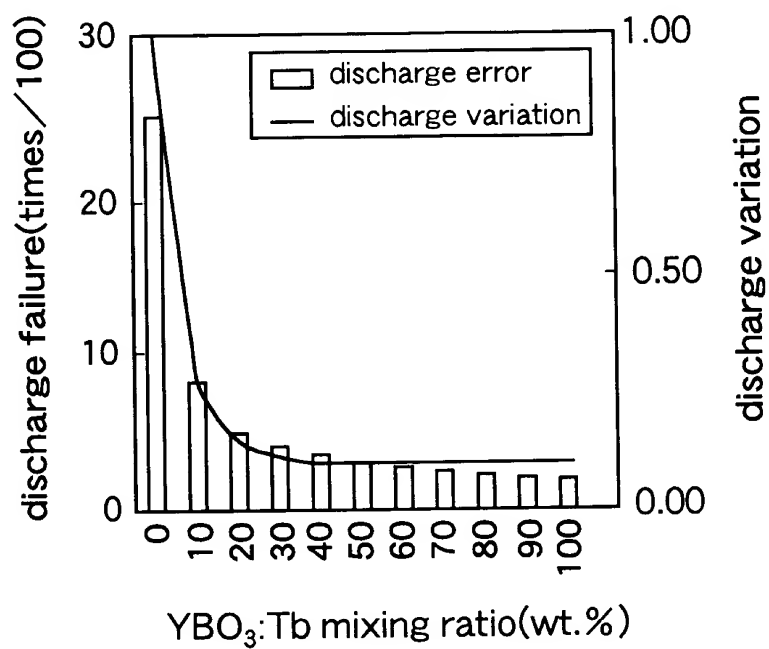


FIG. 8

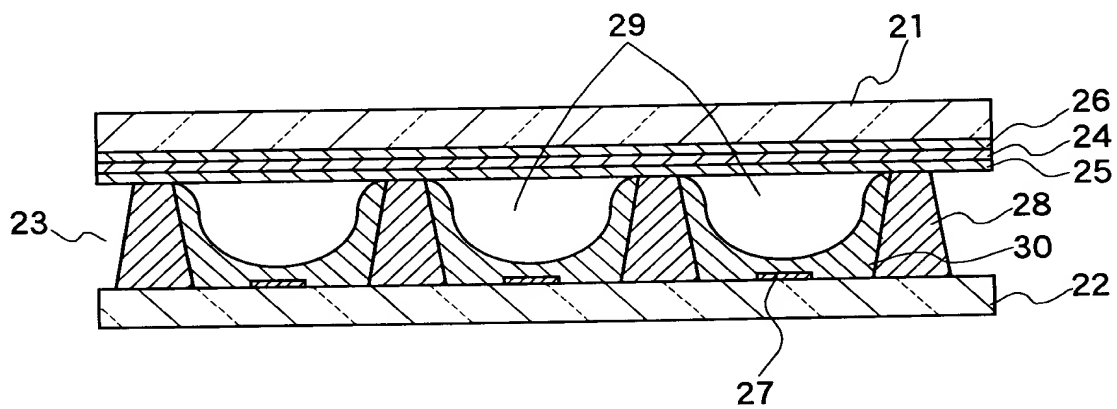


FIG. 9
PRIOR ART

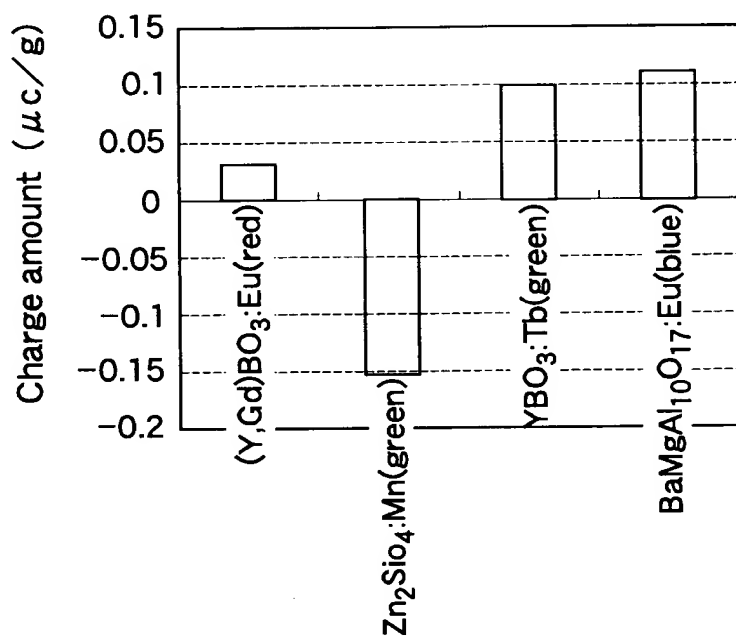


FIG. 10
PRIOR ART